



Sheet 1 of 2

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|---|--|--|---------------------------------------|-------------------------------|
| Substitute Form PTO-1449<br>(Modified)  |  | U.S. Department of Commerce<br>Patent and Trademark Office | Attorney's Docket No.<br>10559-109002 | Application No.<br>10/625,584 |
| <b>Information Disclosure Statement<br/>by Applicant<br/>(Use several sheets if necessary)</b><br><br>(37 CFR §1.98(b)) |  | Applicant<br>Lawrence S. Uzelac, et al.                    |                                       |                               |
|   |  | Filing Date<br>July 22, 2003                               | Group Art Unit                        |                               |

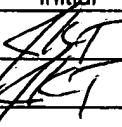
| U.S. Patent Documents |           |                 |                  |                      |       |          |                            |
|-----------------------|-----------|-----------------|------------------|----------------------|-------|----------|----------------------------|
| Examiner Initial      | Desig. ID | Document Number | Publication Date | Patentee             | Class | Subclass | Filing Date If Appropriate |
| <i>JKI</i>            | AA        | 5,838,171       | 11/17/1998       | Davis                |       |          |                            |
|                       | AB        | 5,909,140       | 06/01/1999       | Choi                 |       |          |                            |
|                       | AC        | 5,917,365       | 06/1999          | Houston, Theodore W. |       |          |                            |
|                       | AD        | 6,191,470       | 02/2001          | Forbes et al.        |       |          |                            |
|                       | AE        | 6,307,233       | 10/2001          | Awaka et al.         |       |          |                            |
|                       | AF        | 6,373,321       | 04/2002          | Yamauchi et al.      |       |          |                            |
|                       | AG        | 6,380,798       | 04/2002          | Mizuno et al.        |       |          |                            |
|                       | AH        | 6,715,090B1     | 03/30/2004       | Tosuka et al.        |       |          |                            |
| <i>JKI</i>            | AI        | 2004/0158756 A1 | 08/12/2004       | Tosuka et al.        |       |          |                            |

| Foreign Patent Documents or Published Foreign Patent Applications |           |                 |                  |                          |       |          |               |
|---|-----------|-----------------|------------------|--------------------------|-------|----------|---------------|
| Examiner Initial  | Desig. ID | Document Number | Publication Date | Country or Patent Office | Class | Subclass | Translation   |
|   |           |                 |                  |                          |       |          | Yes No        |
| <i>JKI</i>  | AJ        | 10189884A       | 07/1998          | Japan                    |       |          | Abstract Only |
|   | AK        | 10-1998-0004940 | 03/30/1998       | Korea                    |       |          | Abstract Only |
| <i>JKI</i>  | AL        | 10-1999-0078182 | 10/25/1999       | Korea                    |       |          | Abstract Only |
|   | AM        |                 |                  |                          |       |          |               |
|   | AN        |                 |                  |                          |       |          |               |

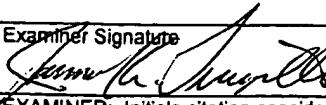
| Other Documents (include Author, Title, Date, and Place of Publication) |           |   |
|---|-----------|---|
| Examiner Initial  | Desig. ID | Document  |
| <i>JKI</i>  | AO        | Anonymous, <i>Diode Battery Back-Up Switch</i> , IBM TECHNICAL DISCLOSURE BULLETIN, VOL. 30, NO. 2, July 1, 1987, pg 895  |
|   | AP        | Muhammad M. Khellah and M.I. Emasry, <i>Power Minimization of High-Performance Submicron CMOS Circuits Using a Dual-V<sub>d</sub> Dual-V<sub>t</sub> (DV<sub>d</sub>V<sub>t</sub>) Approach</i> , PROCEEDINGS 1999 INTERNATIONAL SYMPOSIUM ON LOW POWER ELECTRONICS AND DESIGN, August 1999, ACM, pp. 106-108 |
|   | AQ        | K. Roy, Leakage power reduction in low-voltage CMOS designs, <i>Electronics, Circuits and Systems, 1998 IEEE International Conference</i> , Vol. 2, 1998, pgs. 167-173  |
|   | AR        | Office Action received in Korean Patent Application No. 10/2002/7008020   |
| <i>JKI</i>  | AS        | Office Action received in Chinese Patent Application No. 00817542.X   |

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|--|-----------------------------|
| Examiner Signature<br><i>James S. Uzelac</i>   | Date Considered<br>12/27/04 |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |                             |

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| <b>Information Disclosure Statement<br/>by Applicant</b><br>(Use several sheets if necessary) |  | Applicant<br>Lawrence S. Uzelac, et al. |                               |
| (37 CFR §1.98(b))   |  | Filing Date<br>July 22, 2003            | Group Art Unit                |

| <b>Other Documents (include Author, Title, Date, and Place of Publication)</b>    |           |   |
|---|-----------|---|
| Examiner Initial  | Desig. ID | Document  |
|  | AT        | International Preliminary Examination Report received in PCT/US00/42597 |
|  | AU        | International Search Report received in PCT/US00/42597                  |

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|--|-----------------------------|
| Examiner Signature<br>  | Date Considered<br>12/27/04 |
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